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>A Taxonomy for **Key Escrow Encryption** Systems

... This proposed enhancement to Clipper offers time-bounded data recovery through a **clock** and date-dependent device **unique keys**....

Key to Table 1. ...

www.cosc.georgetown.edu/~denning/crypto/Taxonomy.html - 36k - [Cached](#) - [Similar pages](#)

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Fun and Unique Clocks

We went cuckoo to offer you a great selection of fun clocks.

www.BaronBob.com

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FAQ - HASP

... is a HASP HL **key** that contains a real-time **clock**. ... Each software vendor is assigned a **unique** vendor code which is burned into the **key** during production. ...

www.ealaddin.com/hasp/faq.asp - 32k - Sep 13, 2004 - [Cached](#) - [Similar pages](#)

STU-III **Secure** Telephone Units, Crypto **Key** Generators, **Encryption** ...

... HF SSB communications networks presents several **unique** problems to ... The transmitting unit selects the appropriate **key** by means of a real time **clock** at a ...

www.tscm.com/stu.html - 101k - [Cached](#) - [Similar pages](#)

[PPT] **Cryptography and Network Security 3/e**

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... timestamps (needs synchronized **clocks**). challenge/response (using **unique** nonce).

Simple example 1. ... usually with a trusted **Key** Distribution Center (KDC). ...

www.cs.uml.edu/~dm/course/91.561/s03/notes/ch13.ppt - [Similar pages](#)

Services & Products > Microelectronics & Electronic Devices > ...

... and 3) analysis using low-frequency **clocks**. ... are such that the **unique** factorization questions ... many application examples of public **key encryption** schemes; RSA ...

www.fujitsu.com/services/microelectronics/technical/fram/casestudy_technical-fram_6.html - 23k - [Cached](#) - [Similar pages](#)

[PDF] **Public Key** book

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... a 26 number **clock** or even a very large **clock** with a ... there is no way to know who left the **key** and money ... the set of numbers used to form a cipher must be **unique**. ...

www.hifn.com/docs/a/The-Magic-of-Public-Key-Encryption.pdf - [Similar pages](#)

News Release - **Encryption**

... Another feature making the DES ASIC **unique** is that it ... with another independent **key** on the fourth **clock** cycle ... gives the device a high degree of **key** and function ...

www.sandia.gov/media/NewsRel/NR1999/encrypt.htm - 15k - [Cached](#) - [Similar pages](#)

ActiveScreenLock - This innovative **security** application disables ...

... Introducing Active Screen Lock, the **unique security** application by ... as even if the Reset **key** is pressed on ... customizable allowing you to display **clock** and even ...

www.programurl.com/activescreenlock.htm - 25k - [Cached](#) - [Similar pages](#)

[PDF] **SafeEnterprise Security** System

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Common management platform • **Unique security** management capabilities ... and OC12c applications • **Key**-agile data ... Frame Encryptor • Supports **clock** speeds up ...

www.safenet-inc.com/Library/3/SafeEnterpriseSecuritySystem_ProductBrief.pdf -

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[PDF] **Encryption and Security Tutorial Security Requirements**

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... exchanging a conventional **encryption key** Hash Functions Creates a **unique** "fingerprint" for ... late-90's hardware 16 stages, tests 1 **key** per **clock** cycle ...

www.cryptapps.com/~peter/part1.pdf - [Similar pages](#)

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JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Predictable timestamp under synchronized clocks in a network
Geng-Sheng Kuo; Jing-Pei Lin;

Information Theory, 1994. Proceedings., 1994 IEEE International Symposium on , 27 June-1 July 1994

Pages:68

[\[Abstract\]](#) [\[PDF Full-Text \(60 KB\)\]](#) **IEEE CNF**
2 VLSI architecture and FPGA implementation of ICE encryption algo
Fournaris, A.P.; Sklavos, N.; Koufopavlou, O.;

Electronics, Circuits and Systems, 2003. ICECS 2003. Proceedings of the 200 IEEE International Conference on , Volume: 1 , 14-17 Dec. 2003

Pages:88 - 91 Vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(1597 KB\)\]](#) **IEEE CNF**
3 Correction data for Navstar Global Positioning System transmitted i RDS channel. Technical verification and implement in Sweden
Bergstrom, C.;

RDS (Radio Data Services) (Digest No. 1994/249), IEE Colloquium on , 15 De 1994

Pages:3/1 - 3/5

[\[Abstract\]](#) [\[PDF Full-Text \(308 KB\)\]](#) **IEEE CNF**
4 Chip-Package Co-Implementation of a Triple DES Processor
Schaffer, T.; Glaser, A.; Franzon, P.D.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on] , Volume: 27 , Issue: 1 , Feb. 2004

Pages:194 - 202

[\[Abstract\]](#) [\[PDF Full-Text \(856 KB\)\]](#) **IEEE JNL**

5 Design-space exploration of a cryptography algorithm

Sotiriou, C.P.; Papaefstathiou, Y.;

Electronics, Circuits and Systems, 2003. ICECS 2003. Proceedings of the 200 IEEE International Conference on , Volume: 2 , 14-17 Dec. 2003

Pages:858 - 861 Vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(1451 KB\)\]](#) **IEEE CNF**

6 A bit-serial implementation of the international data encryption algorithm IDEA

Leong, M.P.; Cheung, O.Y.H.; Tsoi, K.H.; Leong, P.H.W.;

Field-Programmable Custom Computing Machines, 2000 IEEE Symposium on 19 April 2000

Pages:122 - 131

[\[Abstract\]](#) [\[PDF Full-Text \(576 KB\)\]](#) **IEEE CNF**

7 Hardware implementation of 128-bit symmetric cipher SEED

Young-Ho Seo; Jong-Hyeon Kim; Dong-Wook Kim;

ASICs, 2000. AP-ASIC 2000. Proceedings of the Second IEEE Asia Pacific Conference on , 28-30 Aug. 2000

Pages:183 - 186

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **IEEE CNF**

8 De-synchronization: asynchronous circuits from synchronous specifications

Sotiriou, C.P.; Lavagno, L.;

SOC Conference, 2003. Proceedings. IEEE International [Systems-on-Chip] , Sept. 2003

Pages:165 - 168

[\[Abstract\]](#) [\[PDF Full-Text \(413 KB\)\]](#) **IEEE CNF**

9 A low device occupation IP to implement Rijndael algorithm [cryptography]

Panato, A.; Barcelos, M.; Reis, R.;

Design, Automation and Test in Europe Conference and Exhibition, 2003 , 20 Pages:20 - 25 suppl.

[\[Abstract\]](#) [\[PDF Full-Text \(577 KB\)\]](#) **IEEE CNF**

10 Design of a scalable RSA and ECC crypto-processor

Ming-Cheng Sun; Chih-Pin Su; Chih-Tsun Huang; Cheng-Wen Wu;

Design Automation Conference, 2003. Proceedings of the ASP-DAC 2003. Asia South Pacific , 21-24 Jan. 2003

Pages:495 - 498

[\[Abstract\]](#) [\[PDF Full-Text \(453 KB\)\]](#) **IEEE CNF**

11 VLSI architecture design and implementation for TWOFISH block cipher
Yeong-Kang Lai; Liang-Gee Chen; Jian-Yi Lai; Tai-Ming Parn;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium
on , Volume: 2 , 26-29 May 2002
Pages:II-356 - II-359 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(408 KB\)\]](#) [IEEE CNF](#)

12 A novel pipelined threads architecture for AES encryption algorithm
Alam, M.; Badawy, W.; Jullien, G.;
Application-Specific Systems, Architectures and Processors, 2002. Proceeding
IEEE International Conference on , 17-19 July 2002
Pages:296 - 302

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) [IEEE CNF](#)

13 A high-throughput low-cost AES cipher chip
Tsung-Fu Lin; Chih-Pin Su; Chih-Tsun Huang; Cheng-Wen Wu;
ASIC, 2002. Proceedings. 2002 IEEE Asia-Pacific Conference on , 6-8 Aug. 20
Pages:85 - 88

[\[Abstract\]](#) [\[PDF Full-Text \(337 KB\)\]](#) [IEEE CNF](#)

14 A reconfigurable linear feedback shift register (LFSR) for the Bluetooth system
Kitsos, P.; Sklavos, N.; Zervas, N.; Koufopavlou, O.;
Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International
Conference on , Volume: 2 , 2-5 Sept. 2001
Pages:991 - 994 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) [IEEE CNF](#)

15 RSA cryptosystem design based on the Chinese remainder theorem
Chung-Hsien Wu; Jin-Hua Hong; Cheng-Wen Wu;
Design Automation Conference, 2001. Proceedings of the ASP-DAC 2001. Asia
South Pacific , 30 Jan.-2 Feb. 2001
Pages:391 - 395

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